


Behzad Mesgarzadeh and Atila Alvandpour: “A 24-mW, 0.02-mm2, 1.5-GHz DLL-Based Frequency Multiplier in 130-nm CMOS”, in Proceedings of IEEE Intl. System-on-Chip Conference (SoCC), pp. 257-260, Austin USA, 24-27 Sept. 2006.


